

AMENDMENT UNDER 37 C.F.R. § 1.111
U.S. APPLICATION NO. 10/728,982
ATTORNEY DOCKET NO. Q78732

Please delete the present Abstract of the Disclosure, and add the following new Abstract of the Disclosure:

A computer program product that runs via a processor system for generating and/or analyzing traffic signals for testing at least parts of integrated-circuit-environments designed to handle traffic signals are provided with generic modules and specific module to increase the re-usability. The specific modules are designed for interfacing the computer program product with a protocol used in the integrated-circuit-environment, such as an Internet-Protocol or an Asynchronous-Transfer-Mode-Protocol or an Ethernet-Protocol traffic protocol, or a flexbus4 or a SPI4.2 bus protocol. The processor system already present in the integrated-circuit-environment and comprising a host processor is used for interfacing with the computer program product, which saves additional hardware, with generated traffic signals flowing from the host processor to a buffer and from the buffer to at least one further circuit of the integrated-circuit-environment.